

CMOL: Freeing Advanced Lithography from the Alignment Requirement Terror

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I will review the recent development of devices and architectures for hybrid CMOS/nanoelectronic circuits using the area-distributed “CMOL” interface. Such circuits may provide unprecedented performance in several applications including memories, digital logic circuits (of both the FPGA and ASIC variety), and bio-inspired mixed-signal networks, while not requiring nanoscale layer alignment.