

Potential of phase shifted optical proximity correction for 65nm T joint pattern in high NA lithography

Songbo Gao^{a,b}, Yanqiu Li^{a,c*}

*a. Institute of Electrical Engineering of Chinese Academy of Sciences
No.6 Bei-Er-Tiao, Zhong Guan Cun, Beijing 100080, P.R. China*

*b. Graduate University of Chinese Academy of Sciences
No.Jia19 Yuquanlu, Shijingshan District, Beijing 100049, P. R. China*

*c. Beijing Institute of Technology
No.5 South Zhongguancun Street, Haidian District, Beijing 100081, P. R. China*

A capable optical proximity correction (OPC) model and phase shift mask (PSM) to meet the requirement of low k1 lithography with the tight dimension control are urgently. However there is a phase confliction in imaging T joint pattern when phase shift mask is used. 65nm T joint pattern is usually imaged with line end shortening and it couldn't be corrected well with mere OPC. New resolution enhancement technology named phase shifted optical proximity correction (PSOPC) is presented in this paper. A PSOPC mask with phase shifted serif modifies both the intensity and the phase of the local incident light simultaneously. The line end shortening pattern was corrected by PSOPC without phase confliction for T joint pattern. PSOPC can provide a better image fidelity and process robustness that could not be realized by introducing traditional OPC or PSM individually or integrating them simply for 65 nm T joint pattern. The results are simulated by Prolith and show that PSOPC as a new RET has much potential for 65nm T joint pattern lithography.

Key words: Lithography simulation, Prolith, Resolution enhancement technology, PSOPC, High NA

Acknowledgments: This research is supported by National Funds of Sciences under Grant No. 10674134 and Grand Fundamental Research 973 Program of China under Grant No. 2003CB716204.

*Corresponding author: Yanqiu Li, Phone/Fax: 86-10-62561490

E-mail: yanqiuli@hotmail.com, liyanq@mail.iee.ac.cn

Reference

1. S. R. J. Brueck, Optical and Interferometric Lithography—Nanotechnology Enablers, Proc. IEEE 93, 1704 (2005).
2. Brian J. Grenon, Grenon Consulting, Mask Costs, A New Look, Proc. SPIE 6281, 628101 (2006).
3. Toshio Konishi, *et al.*, Through-pitch and through-focus characterization of AAPSM for ArF immersion lithography, Proc. SPIE 6281, 62810S (2006).
4. Piotr Berman, *et al.*, Optimal Phase Conflict Removal for Layout of Dark Field Alternating Phase Shifting Masks, IEEE Trans. CAD of Integrated Circuits and System 19, 175 (2000).
5. K. Honda, *et al.*, Design Rule Optimization for 65nm-node (CMOS5) BEOL Using Process and Layout Decomposition Methodology, Proc. SPIE 5379, 111 (2004).
6. Paul.J.M. Van Adrichem, Manoj Chacko, Bryan S. Kasrowicz, An efficient Resolution Enhancement Technique flow for 65nm Logic Poly layer, Proc. SPIE 5853, 109 (2005).

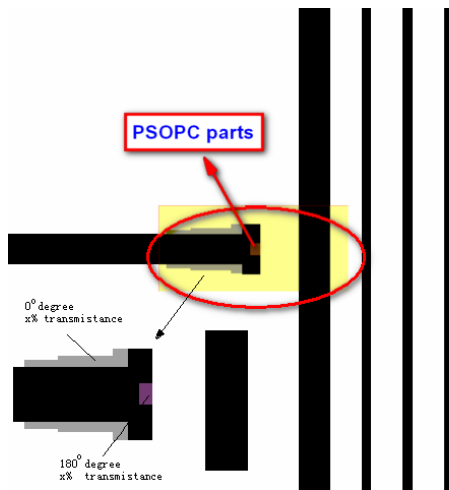


FIG.1. Mask with PSOPC

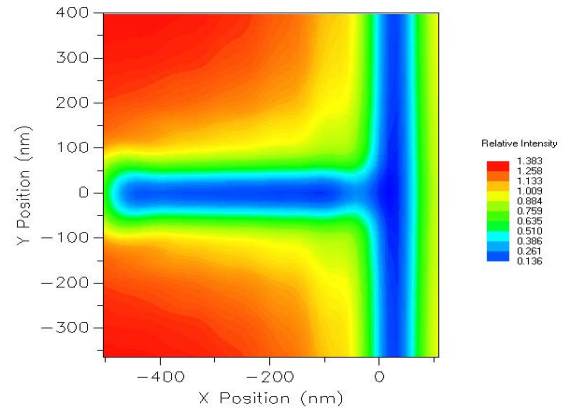


FIG.2. Resist T joint pattern with PSOPC

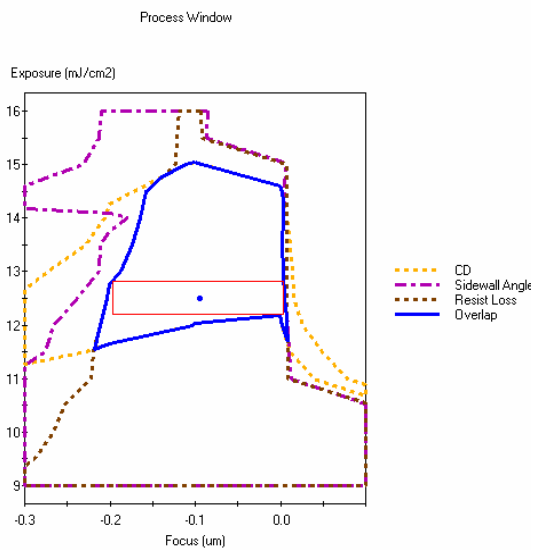


FIG.3. Process window (@5%EL)

$$\lambda = 193.368nm$$

$$NA = 0.95(\text{water immersion})$$

$$\sigma = 0.60/0.22(\text{Annular Illumination})$$

Resist : ArF JSR AR165J, thickness=200nm

BARC: Shipley AR19 ,thickness=48 nm