

## Template based fabrication of size tunable single-walled carbon nanotube single electron transistors

Saiful I. Khondaker, Paul Stokes, Daeha Joung, and Yodchay Jompol  
Nanoscience Technology Center & Department of Physics, University of Central  
Florida, 12424 Research Parkway, Orlando FL 32826, USA

Single electron transistors (SETs) have attracted considerable attention because of their potential as a building block for future quantum based nanoelectronic devices. However fabrication of reproducible and controllable quantum dot sizes that can operate at high temperature is challenging. We developed a novel technique for the fabrication of size tunable and controllable quantum dot using single-walled carbon nanotube<sup>1</sup>. Our technique is based on the formation of two tunnel barriers of controllable separation by naturally bending SWNT at the edges of a electron beam lithographically patterned local Al/Al<sub>2</sub>O<sub>3</sub> gate of length  $L$ . A SWNT is placed on top of the local gate, and then contacted with Pd source and drain electrodes of 1  $\mu\text{m}$  separation on Si/SiO<sub>2</sub> substrates. The aluminum gate serves three purposes: (i) it acts as a “mechanical template” to define two tunnel barriers at the edges by naturally bending the nanotube due to van der Waals interactions with the substrate, (ii) the width of the gate defines the size ( $L$ ) of the quantum dot, and (iii) it acts as a local bottom gate to control the operation of the SET device. Using this approach we fabricated SETs of different sizes from 500 nm down to 50 nm. We present detailed fabrication procedures and low temperature transport studies of these SET devices.

---

<sup>1</sup> Paul Stokes and Saiful I. Khondaker, Appl. Phys. Lett. 92, 262107 (2008).

