Transfer-Printing of Prepatterned Semiconducting Few-Layer-Molybdenum Disulfide Structures for Electronic Applications

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Molybdenum disulfide (MoS₂), previously widely used as a lubricant material, recently attracts a great deal of attention because of its attractive electronic, optoelectronic, and mechanical properties.¹⁻³ Especially, monolayer and few-layer MoS₂ films have a large direct bandgap that is suitable for semiconductor-related applications such as thin-film transistors $(TFTs)^{f}$, chemical sensors⁴, and light emission devices². Such atomically layered films also exhibit a high mechanical flexibility and can be used for making flexible electronic products with high performance.³ The current methods for producing few-layer MoS_2 flakes include scotch tape exfoliation,⁵ chemical vapor deposition (CVD),⁶ and laser-thinning process⁷ etc. These methods still suffer from specific disadvantages and cannot create ordered, pristine MoS_2 device arrays over large areas that are required for large-area applications. Therefore, novel low-cost, upscalable nanofabrication methods are needed for addressing such manufacturing-related issues and enabling the future scale-up applications of MoS_2 in electronics and optoelectronics. In this work, we systematically studied transfer-printing approaches for creating orderly arranged MoS_2 micro- and nanostructures over large (cm²scale) areas and demonstrated working field-effect transistors (FETs) made from printed MoS₂ flakes with excellent transistor performance. This research also identified the key processing conditions affecting the printing uniformity over large areas, morphologies of printed MoS₂ structures, and ultimate transport properties of MoS₂-based FETs.

In our processes, the device patterns are firstly pre-structured onto a bulk MoS_2 disc by using photolithography followed with plasma etching. This MoS_2 disc is then used as a relief template for exfoliating prepatterned MoS_2 flake arrays over large areas *via* various printing processes, including direct mechanical printing⁸, plasma-assisted printing, and electrostatic printing⁹. All of these printing processes can produce ordered MoS_2 arrays over large areas, but different printing processes can result in different morphologies of individual MoS_2 flakes, as demonstrated in Fig. 1. In addition, the MoS_2 flakes created by various transfer-printing processes exhibit different transport properties. For example, Figs. 2a and 2b show $I_{DS}-V_{DS}$ and $I_{DS}-V_G$ characteristic curves, respectively, of a MoS_2 FET produced by using electrostatic printing, which exhibit N-type conduction. Figure 2c shows the $I_{DS}-V_G$ characteristic curve of another MoS_2 FET made by plasma-assisted printing, which exhibits P-type conduction.

Our work demonstrated the printing of high-quality, well-defined MoS₂ flakes over large areas and working MoS₂-based FETs with excellent performance. The fundamental knowledge achieved in this work could also be used for optimizing the printing-based manufacturing routes for producing other atomically layered materials and devices.

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Fig. 1 SEMs of MoS_2 micro-/nanostructures produced by various transfer-printing processes: (a) microscale flakes with well-defined edges but broken central areas created by mechanical printing, (b) 300 nm wide rings with large-area uniformity created by plasma-assisted transfer-printing, and (c) microscale flakes with continuous MoS_2 films created by electrostatic printing. All the scale bars are 50 µm.



Fig. 2 Transport characteristics of MoS_2 -based FETs made from transfer-printed MoS_2 flakes. (a) I_{DS} - V_{DS} and (b) I_{DS} - V_G characteristic curves of an N-channel MoS_2 FET; (c) I_{DS} - V_G characteristic curve of a P-channel MoS_2 FET made by using plasma-assisted transfer-printing.

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