

Plasma-Assisted Doping for Fabricating MoS₂ Diodes and Ambipolar Transistors

Mikai Chen, Hongsuk Nam, Sungjin Wi, and Xiaogan Liang*

Department of Mechanical Engineering, University of Michigan, Ann Arbor, MI 48109

Molybdenum disulfide (MoS₂) and other two-dimensional (2D) layered transition metal dichalcogenides (LTMDs) recently received a great deal of interest due to their excellent electronic, optoelectronic and mechanical properties, versatile chemistry, as well as large abundance of relevant bulk materials.^{[1]-[4]} Especially as a 2D semiconductor, MoS₂ can be used for making phototransistors,^[5] highly sensitive sensors,^[6] high-performance thin-film transistors (TFTs) that are immune to short-channel effects,^[7] and ultra-thin flexible photovoltaic (PV) cells and other relevant optoelectronic devices.^[8] However, people now lack proper doping methods for creating permanently stable p-n junctions and rectifying diode structures in MoS₂ and other relevant 2D semiconductors, which seriously limits their practical applications, especially the long-term electronic and optoelectronic applications of such 2D materials at ambient conditions.^{[9][10]}

To address this challenge, we developed a new plasma-assisted doping method for modulating the electronic properties of MoS₂ and other relevant 2D semiconductors. Using blank plasma doping processes, we have demonstrated p-type MoS₂ transistors that can be complementary to undoped n-type MoS₂ transistors, potentially enabling MoS₂-based CMOS circuits. Using selected-area plasma doping processes, we have created permanently stable *p-n* junctions in MoS₂ layers and 2D diodes with a high rectification degree (*i.e.*, forward/reverse current ratios $\sim 10^4$).^[11] These plasma-doped devices are anticipated to play an important role in the development of new nanoelectronic devices based on emerging 2D semiconductors.

Fig. 1 shows the SEM image of an exemplary MoS₂ rectifying diode fabricated by doping a selected MoS₂ channel area with a CF₄-based plasma recipe. Such plasma-doped diodes exhibit a high rectification degree (*i.e.*, forward/reverse current ratios $\sim 10^4$ within a range of $V_{DS} = \pm 1$ to ± 2 V) and a superior long-term stability at room temperature, as shown in Fig. 2. Fig. 3 shows that the presented plasma-assisted doping process can also be used for making MoS₂-based ambipolar and *p*-type transistors. The final presentation will also include our in-depth X-ray photoelectron spectroscopic (XPS) characterizations of MoS₂ layers treated with different plasma species, aiming to understand the mechanisms of plasma-induced doping at atomic scales.

This work could serve as an important foundation for exploration of new physical and/or chemical mechanisms for tailoring the band structures of emerging 2D semiconductor micro-/nanostructures to achieve desirable characteristics for device applications in the fields of renewable energy, nanoelectronics, and optoelectronics.

[1] Radisavljevic, B., et al. Nature nanotechnology 6.3 (2011): 147-150.

[2] Pu, Jiang, et al. Nano letters 12.8 (2012): 4013-4017

[3] Korn, Tobias, et al. Applied Physics Letters 99.10 (2011): 102109-102109.

[4] Mak, Kin Fai, et al. Physical Review Letters 105.13 (2010): 136805.

[5] Yin, Zongyou, et al. ACS nano 6.1 (2011): 74-80.

[6] He, Qiyuan, et al. Small 8.19 (2012): 2994-2999.

[7] Wang, Jing, and Mark Lundstrom Electron Devices Meeting, 2002. IEDM'02. International. IEEE, 2002.

[8] Xiao, Di, et al. Physical Review Letters 108.19 (2012): 196802.

[9] Fang, Hui, et al. Nano letters 13.5 (2013): 1991-1995.

[10] Zhang, Y. J., et al. Nano letters 13.7 (2013): 3023-3028.

[11] Chen, Mikai, et al. Applied Physics Letters 103.14 (2013): 142110.

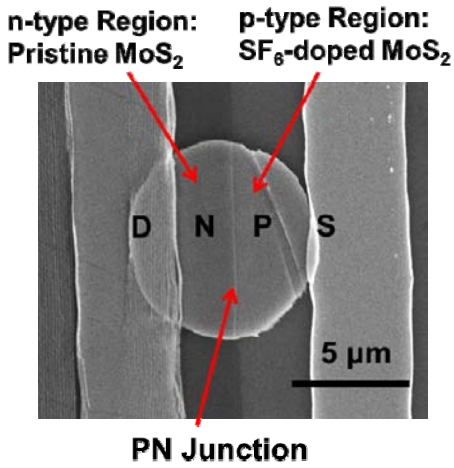


Fig. 1 SEM image of a fabricated MoS₂ rectifying diode with selected area treated by CF₄ plasma.

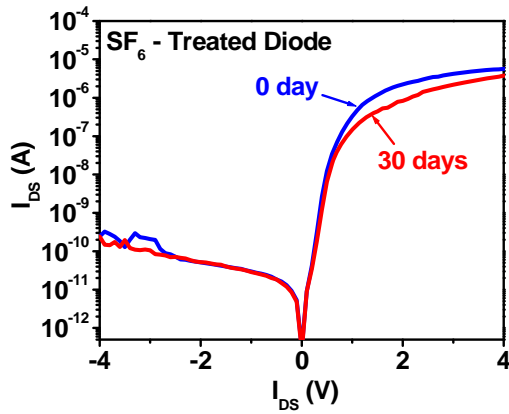


Fig. 2 Semi-logarithmic plots of I_{DS} - V_{DS} characteristic curves of a SF₆-treated diode, which were measured 0 (dashed line) and 30 (solid line) days after the device fabrication.

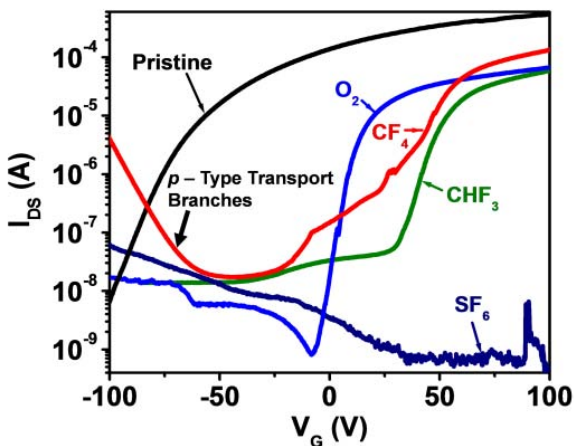


Fig. 3 I_{DS} - V_G characteristics of back-gated MoS₂ FETs treated with different plasma recipes as well as an untreated pristine FET (for all curves, $V_{DS} = 5$ V).