## Suspended two-dimensional MoS<sub>2</sub> transistor

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Two-dimensional (2D) materials attract increasing attention, due to their ultralow weight, high mobility, extraordinary mechanical properties and flexibility [1]. An intrinsic bandgap within the 2D semiconducting transition metal dichalcogenides (TMDs) [2,3], such as MoS<sub>2</sub> and WSe<sub>2</sub>, makes them more promising in the electronic and optoelectronic devices applications. However, the trapped charges at the interface of 2D materials/substrates can screen the gate field and give rise to the Coulomb scattering [4], which negatively affect the carrier transport in the 2D materials. Thus, understanding of the intrinsic characteristics of MoS<sub>2</sub> field-effect transistors (FETs) without the effect of underlying substrate is essential [5].

A back gated  $MoS_2$  FET has been fabricated on Si/SiO<sub>2</sub> substrate, as shown in Figure 1(a). After the 2D MoS<sub>2</sub> has been exfoliated and transferred onto the substrate, the 2D MoS<sub>2</sub> channel has been patterned with optical lithography and CF<sub>4</sub> plasma etching. The metal contact regions have been defined on negative photoresist with the help of alignments marks in SiO<sub>2</sub>[6]. Then Ti/Al metal stacks have been deposited by E-beam evaporation and lifted off. The device has been annealed in forming gas (N<sub>2</sub>/H<sub>2</sub>) at 300°C for 2 hours to remove adsorbed water and organic contamination before characterized in probe station. Figure 1(b) depicts the optical image of the fabricated MoS<sub>2</sub> FET that enables 4 point measurements. The fabricated MoS<sub>2</sub> FET exhibits the notable n-type semiconducting behavior with the  $\sim 15$  V threshold voltage, as shown in Figure 1(c). A mild hysteresis is observed which is attributed to charge-trapping effects at the MoS<sub>2</sub>/SiO<sub>2</sub> interface. The mobility of MoS<sub>2</sub> channel has been extracted as 1.76 cm<sup>2</sup>V/s and 2.59 cm<sup>2</sup>V/s from 2 point and 4 point measurements, respectively. From the  $I_{DS}$ - $V_{DS}$  curves shown in Figure 1(d, e), the resistance of MoS<sub>2</sub> channel is determined to be 2.75  $\Omega$ ·cm (2 point measurement) and 2.03  $\Omega$ ·cm (4 point measurement) at a gate bias  $V_{GS}$  of 60 V. The above results show that the contact resistance between MoS<sub>2</sub> and Ti (2.7 M $\Omega$  when V<sub>GS</sub>=60 V) possibly resulting from the Schottky barrier plays an important role in the current transport of MoS<sub>2</sub> FET. Therefore, in order to extract the intrinsic mobility of MoS<sub>2</sub>, 4 point measurement needs to be applied.

In order to compare the mobility and resistance of  $MoS_2$  sheet on the substrate and released from the substrate, the  $MoS_2$  channel will be suspended with a vapour HF etching system (MEMSSTAR) and electrical characterization will be performed on the suspended  $MoS_2$  FET. In addition, the effect of substrate on the hysteresis of FET will be investigated.

<sup>[1]</sup> R. Zhang, V. Koutsos, and R. Cheung, Appl Phys Lett 108, 5 (2016).

<sup>[2]</sup> W. Zhao, Z. Ghorannevis, L. Chu, M. Toh, C. Kloc, P.-H. Tan, and G. Eda, Acs Nano 7, 791 (2012).

<sup>[3]</sup> Q. H. Wang, K. Kalantar-Zadeh, A. Kis, J. N. Coleman, and M. S. Strano, Nat Nanotechnol 7, 699 (2012).
[4] Y. Guo, X. L. Wei, J. P. Shu, B. Liu, J. B. Yin, C. R. Guan, Y. X. Han, S. Gao, and Q. Chen, Appl Phys Lett 106, 103109 (2015).

<sup>[5]</sup> Z. Zhang, K. Yao, Y. Liu, C. Jin, X. Liang, Q. Chen, and L. M. Peng, Adv Funct Mater 17, 2478 (2007).

<sup>[6]</sup> R. Zhang, T. Chen, A. Bunting, and R. Cheung, Microelectron Eng Submitted (2016).



Figure 1: (a) Schematic of the back gated  $MoS_2$  FET fabrication. (b) Optical image of the fabricated  $MoS_2$  FET. (c) Transfer characteristics of the  $MoS_2$  FET obtained from two/four point measurements. Output characteristics of the  $MoS_2$  FET obtained from two point (d) and four point (e) measurements. (f) Schematic of a suspended  $MoS_2$  FET.