

# Fabrication of Silicon Waveguides using Metal-Assisted Chemical Imprinting

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Scalable manufacturing of silicon (Si) waveguides is important for the development of the integrated optoelectronic systems. Nowadays Si waveguides are fabricated with photolithography methods<sup>1,2</sup> followed by reactive ion etching<sup>3</sup>. These methods, however, affect the manufacturing scalability and introduce sidewall roughness<sup>3</sup>, which reduces waveguide's performance. Recently developed Metal-Assisted Chemical Imprinting<sup>4</sup> (Mac-Imprint) has the potential to overcome these limitations. Mac-Imprint combines contact-based nature of nanoimprint lithography<sup>5</sup> and catalyst-induced Si dissolution of metal-assisted chemical etching<sup>6</sup>. In particular, stamps, coated with thin film of gold (Au) are pressed against Si substrate by external forces in the presence of etching solution. The dissolution of Si is catalyzed at the points of microscopic contact between Au and Si and, thus, the inverse morphology of the stamp is transferred onto Si. Mass-transport of the reactants throughout microscopic Au/Si contact interface is crucial to assure pattern transfer fidelity<sup>7</sup>. To promote the mass-transport during Mac-Imprint of Si waveguides this work utilized stamps covered with nanoporous Au (np-Au), which was made by short-time scale dealloying of Au-Ag alloy. This approach drastically improved pattern transfer fidelity in comparison to the solid Au-covered stamps, however the imprinted Si surface exhibited roughness, which was correlated to the pore size of the np-Au. The pore size of the np-Au was then reduced through the adjustments of dealloying regime in order to approach the resolution limit of Mac-Imprint. This allowed to suppress the catalyst-induced roughness down to 10 nm, thus validating the applicability of Mac-Imprint for fabrication of Si waveguides.

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<sup>1</sup> Y. A. Vlasov et al., "Losses in single-mode silicon-on-insulator strip waveguides and bends," *Optics Express*, vol. 12, no. 8, pp. 1622-1631, 2004.

<sup>2</sup> P. Dumon et. al., "Low-loss SOI photonic wires and ring resonators fabricated with deep UV lithography," *IEEE Photonics Technology Letters*, vol. 16, no. 5, pp. 1328-1330, 2004.

<sup>3</sup> Y. Q. Fu et. al., "Deep reactive ion etching as a tool for nanostructure fabrication," *Journal of Vacuum Science & Technology B: Microelectronics and Nanometer Structures Processing, Measurement, and Phenomena*, vol. 27, no. 3, pp. 1520-1526, 2009.

<sup>4</sup> B. P. Azeredo et. al., "Direct imprinting of porous silicon via metal-assisted chemical etching," *Advanced Functional Materials*, vol. 26, no. 17, pp. 2929-2939, 2016.

<sup>5</sup> S. Y. Chou et al., "Nanoimprint lithography," *Journal of Vacuum science & Technology B: Microelectronics and Nanometer Structure Processing, Measurement, and Phenomena*, vol. 14, no. 6, pp. 4129-4133, 1996.

<sup>6</sup> X. Li et al., "Metal-assisted chemical etching in HF/H<sub>2</sub>O<sub>2</sub> produces porous silicon," *Applied Physics Letters*, vol. 77, no. 16, pp. 2572-2574, 2000.

<sup>7</sup> A. Sharstniou et al., "Electrochemical nanoimprinting of silicon," *Proceedings of the National Academy of Sciences*, vol. 116, no. 21, pp. 10264-10269, 2019.